Stateful Many-cores Network Processing

Dr. Amir Rosen
EZchip, Israel

Thursday, 25 December 2014, at 16:30
EE, Room 1061

Abstract: Network processing is one of the applications most suitable for multi-core parallel processing. This is so due to the innate parallelism of network processing – it deals with a stream of packets where each packet can be processed almost independently. This approach becomes more challenging when stateful processing is needed – i.e. when the processing of a given packet can affect other packets, making the parallelism of the process no longer pure.

In this talk we will discuss approaches to handle the above challenge using EZchip NPS as an example platform. We will also be touching the latest buzzwords of SDN and NFV and see how they interact with the discussed approaches.

The seminar will take 1.5 hours